

synchro/resolver angle position indicator auto-ranging 5 decade

AP-501

FEATURES

- Auto-ranging synchro and resolver inputs
- 47-3000Hz reference frequency range
- High, balanced input impedance
- 0.01° resolution/±0.03° accuracy
- Automatic phase shift compensation
- 0° to 359.99° and 0° to ±180.00° angle ranges
- Bright 0.56" LED display
- Binary and BCD outputs
- Diagnostic signals
- Universal AC input 85 to 265VAC, 47 to 440Hz
- Optional linear DC output

APPLICATIONS

- Antenna Range Instrumentation
- System Test
- Automatic Test Equipment

GENERAL DATA

The AP-501 is an auto-ranging synchro/resolver angle position indicator. Analog synchro or resolver position information is presented on a bright 5 digit LED display to an accuracy of $\pm 0.03^{\circ}$. The display range can be selected to read either 0° to 359.99° or 0° to $\pm 180.00^{\circ}$. A 3-state binary and BCD interface is available at a rear panel connector.

The heart of the AP-501 is a tracking type synchro/resolver to digital converter that incorporates a "reference synthesizer" required to maintain high accuracy even with large rotor to stator phase shifts. The unit employs ratiometric conversion techniques to provide signal amplitude insensitivity. A Type II servo loop is employed to ensure high-noise immunity and jitter-free operation.

The AP-501 incorporates many user-friendly features. The indicator is auto-ranging and operates over a frequency range of 47 to 3000Hz allowing for the measurement of all common synchros and resolvers. The indicator will warn the user of loss of reference excitation, loss of stator signals and excessive indicator error. A universal AC input, 85 to 265VAC at 47 to 440Hz, allows for usage virtually anywhere in the world.

FRONT PANEL CONTROLS

The AP-501 contains three front panel user controls: a Power ON/OFF switch, a Mode switch and a Range switch. The Power ON/OFF switch controls the AC line that powers the unit. The Mode switch configures the indicator to accept either synchro or resolver input voltages. The Range switch selects the display and BCD output for either 0° to 359.99° or 0° to \pm 180.00°.

SYNCHRO/RESOLVER INTERFACE

When in the Synchro mode, the convention for the inputs are as follows:

- $E_{S1-S3} = E_{RL-RH} SIN \Theta$
- $\mathsf{E}_{\mathsf{S3-S2}} = \mathsf{E}_{\mathsf{RL-RH}} \,\mathsf{SIN} \,(\Theta + 120^\circ)$

 $E_{S2-S1} = E_{RL-RH} SIN (\Theta + 240^{\circ})$

When in the Resolver mode, the convention for the inputs are as follows:

 $E_{S1-S3} = E_{RL-RH} SIN \Theta$

 $E_{S4-S2} = E_{RL-RH} COS \Theta$

The synchro or resolver may be connected to the AP-501 via the front panel four-way binding posts or via the rear panel Synchro/Resolver connector. The indicator does not contain a reference excitation supply; therefore, an external AC reference source must be supplied to the RH-RL inputs.

DIAGNOSTIC SIGNALS

The AP-501 has its own built-in diagnostic circuitry that continuously monitors for the following fault conditions:

- 1. Excessive indicator error (FLT)
- 2. Loss of reference excitation (LOR)
- 3. Loss of stator signal voltage (LOS)

If any one of these conditions is sensed, the front panel decimal display is blanked and is replaced by fault designators, see figure 1. Certain fault conditions can cause multiple fault designators to be displayed. In that event, the probable fault is indicated by the left-most fault designator. Once the source of the fault is removed, the fault designators will automatically be replaced by the decimal display.



SPECIFICATIONS

Parameter	Va
Accuracy	±0
Angular Range	
Unipolar	0°
Bipolar	0°
Display	
Number of Digits	5 c
Size of Digits	0.5
Type and Color	7-s
Reference Input (RH-RL)	
Туре	Au
Voltage Range	
Resolver Mode	2-3
Synchro Mode	8-1
Frequency Range	47
Input Impedance	80
Stator Inputs (S1-S2-S3-S4)	
Input Type	Au
Voltage Range	
Resolver Mode	2-3
Synchro Mode	8-1
Rotor/Stator Phase Shift	±5
Input Impedance	
Resolver	50
Synchro	25
Dynamic Characteristics	
Low Bandwidth	
Tracking Rate	90
Ka	1,0
High Bandwidth	
Tracking Rate	18
Ka	35
Digital Inputs	
Type	ΤT
Loading	0.5
Inhibit	"0"
Range	"0"
Select Bandwidth	"0"
Output Control 1	"0"
Output Control 2	"0"
Output Control 3	"0"
Digital Outputs	"0"
Type	
Drive Capability	TT
Binary Angle	5 s
BCD Angle	16
Sign	5 c
Converter Busy	"0"
Fault	1-2
Loss of Reference	"0"
Loss of Stator Signals	"0"
DC Output (Optional)	"0"
Unipolar	• •
Bipolar	0 t
Resolution	0 t
Accuracy	0.0
Power Input	±0
Voltage Range	05
Frequency Range	85 47
Power	47
Operating Temp. Range	10 0°
	0

Value .03° to 359.99° to ±180.00° decimal digits 56" high segment red LED uto-ranging 32Vrms 130Vrms -3000Hz 0KΩ ito-ranging synchro/resolver 32Vrms 130Vrms L-L 50° max. 0K Ω balanced 0KΩ L-L balanced 0°/second 000 00°/second ,000, L compatible 5 std. TTL loads max. "=freeze "1"=track =unipolar "1"=bipolar "=BCD "1"=binary "1"=high "=low "=D00-D07 "1"=Hi-Z "=D08-D15 "1"=Hi-Z "=D16-D23 "1"=Hi-Z L compatible std. TTL loads max. bit positive logic decade positive logic plus sign '=(+) "1"=(-) 2µsecond positive pulse =normal "1"=excessive error '=present "1"=absent '=present "1"=absent to +9.9994V for 0 to 359.99° to ±5.0000V for 0 to ±180.00° 022°).1°

85-265VAC 47-440Hz 10 watts 0° to +50°C

DIGITAL INPUTS

Inhibit

The Inhibit (INH) input is used to freeze the display, binary and BCD outputs. When INH is at logic "1" or open, the display and digital outputs track the synchro or resolver input; when at logic "0", they are latched.

Range

The Range (RNG) input is used to set the display and BCD output angle range. A logic "0" sets to unipolar range and a logic "1" sets to bipolar range. When using this input, set front panel RANGE switch to the $\pm 180^{\circ}$ position, leave input open for front panel control.

Bandwidth

The Bandwidth (BW) input is used to set the dynamic response of the AP-501. A logic "0" or open sets the unit to the low bandwidth and a logic "1" sets it to the high bandwidth. The unit employs a Type II servo loop converter; so as long as the maximum tracking rate is not exceeding, there will be no velocity lag and only minor acceleration lag at the binary output. Acceleration lag (in degrees) can be calculated from the following equation:

$$Ea = \frac{Acceleration (°/sec2)}{Ka}$$

See the chart below for the dynamic characteristics for the two bandwidths.

DYNAMIC CHARACTERISTICS						
BANDWIDTH		MAX TRACKING	Ka	SETTLING TIME*		
LOW HIGH	47-3000Hz 360-3000Hz		1,000 35,000	2.5sec 200msec		

*Settling time for 179° step

Select

The Select (SEL) input places either binary or BCD data on the D00-D23 digital output lines, see figure 2. A logic "0" selects BCD data and a logic "1" or open selects binary data.

Output Control

The Output Control lines (OC1, OC2 and OC3) are used to select the three byte outputs: D00-D07, D08-D15 and D16-D23, see figure 2. A logic "0" enables the corresponding byte output and a logic "1" or open places the outputs in the high impedance state.

DIGITAL OUTPUTS

The AP-501 provides 24 3-state output data lines that can be configured as a single word or any combination of 8 bit bytes. See Truth Table, figure 2, for configuration.

Binary/BCD Outputs

Either a 16 bit parallel binary angle or a 5 decade parallel BCD angle can be selected. The binary angle is a real time word and may be transferred reliably at synchro/resolver rotational rates of up to specified maximum tracking. A simple method of synchronously transferring data is to: a) set INH to logic "0", b) wait 2µsec minimum, c) transfer binary data and d) set INH to logic "1". Asynchronous loading is accomplished by transferring the data on the trailing edge of the CB pulse.

The BCD angle output has a sampling rate of 16 samples a second minimum. This output is only useful for static readings or for driving remote displays.

Fault

The synchro/resolver to digital converter is continuously monitored for proper operation. If a converter failure or over-velocity condition is detected, the Fault (FLT) output is set to logic "1".

DISPLAY FAULT DESIGNATORS FIGURE 1



TRUTH TABLE FIGURE 2

OUTPUT	SELECT		00	 C1	00	22	00	23
	_ L	H	L	Н	L	Н	L	Н
D00	0.01°	0.005°	ΕN	Ζ	Х	Х	Х	Х
D01	0.02°	0.011°	EN	Ζ	Х	Х	Х	X
D02	0.04°	0.022°	EN	Ζ	X	Х	Х	X
D03	0.08°	0.044°	EN	Ζ	Х	Х	Х	X
D04	0.1°	0.088°	EN	Ζ	Х	Х	Х	X
D05	0.2°	0.176°	ΕN	Ζ	X	Х	Х	X
D06	0.4°	0.352°	ΕN	Ζ	X	Х	Х	X
D07	0.8°	0.703°	ΕN	Ζ	X	Х	Х	X
D08	1°	1.406°	Х	Х	EN	Ζ	Х	X
D09	2°	2.812°	Х	Х	EN	Ζ	Х	X
D10	4°	5.625°	Х	Х	EN	Ζ	Х	X
D11	8°	11.25°	Х	Х	EN	Ζ	Х	X
D12	10°	22.50°	Х	Х	EN	Ζ	Х	X
D13	20°	45.00°	Х	Х	EN	Ζ	Х	X
D14	40°	90.00°	Х	Х	EN	Ζ	Х	X
D15	80°	180.0°	Х	Х	EN	Ζ	Х	X
D16	100°	"0"	Х	Х	Х	Х	EN	Z
D17	200°	"0"	Х	Х	Х	Х	EN	Z
D18	SIGN	CB	Х	Х	X	Х	EN	Z
D19	FLT	FLT	Х	Х	X	Х	EN	Z
D20	LOR	LOR	Х	Х	Х	Х	EN	Z
D21	LOS	LOS	Х	Х	Х	Х	EN	Z
D22	"0"	"0"	Х	Х	Х	Х	EN	Z
D23	"0"	"0"	Х	Х	Х	Х	ΕN	Ζ

H=high level L= low level X=irrelevant Z=hi-impedance EN=enabled "0"=logic 0

Loss of Reference Excitation

The reference excitation input is also monitored. When the reference excitation is present, the LOR output is a logic "0". If the reference is not present, the LOR output will be a logic "1".

Loss of Stator Voltage

The stator input signals are monitored and when the signals are present, the LOS output is a logic "0". If these signals are not present, the LOS output will be a logic "1".

CALIBRATION

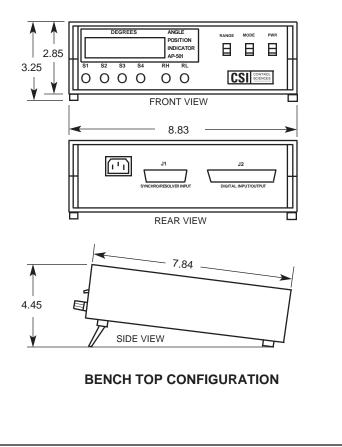
It is recommended calibration be checked once a year. A Calibration Test Procedure will be furnished upon request.

PIN ASSIGNMENTS

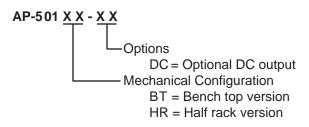
SYNCHRO/RESOLVER INPUT J1				
PIN#	FUNCTION			
1	S1			
2	S2			
3	S3			
4	S4			
5	RH			
6	RL			

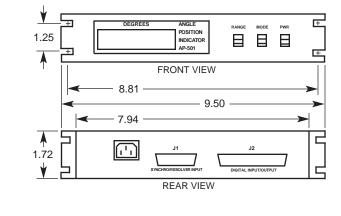
	DIGITAL INPUT/OUTPUT J2						
FUNCTION	PIN#	FUNCTION	PIN#	FUNCTION			
N/C	14	D19	27	OC1			
GND	15	D17	28	D09			
GND	16	D22	29	D11			
D01	17	D21	30	D13			
D03	18	D20	31	D15			
D04	19	INH	32	D18			
D05	20	DC Out	33	OC3			
D07	21	GND	34	D16			
D08	22	D00	35	D23			
D10	23	D02	36	BW			
D12	24	RGN	37	AGND			
D14	25	SEL					
OC2	26	D06					
	N/C GND GND D01 D03 D04 D05 D07 D08 D10 D12 D14	N/C 14 GND 15 GND 16 D01 17 D03 18 D04 19 D05 20 D07 21 D08 22 D10 23 D12 24 D14 25	N/C 14 D19 GND 15 D17 GND 16 D22 D01 17 D21 D03 18 D20 D04 19 INH D05 20 DC Out D07 21 GND D08 22 D00 D10 23 D02 D12 24 RGN D14 25 SEL	N/C 14 D19 27 GND 15 D17 28 GND 16 D22 29 D01 17 D21 30 D03 18 D20 31 D04 19 INH 32 D05 20 DC Out 33 D07 21 GND 34 D08 22 D00 35 D10 23 D02 36 D12 24 RGN 37 D14 25 SEL			

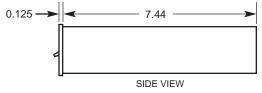
MECHANICAL OUTLINE



ORDERING INFORMATION







HALF RACK CONFIGURATION

NOTES:

- 1. Dimensions are in inches.
- 2. J1 is a DA-15P type connector with jack nuts.
- 3. J2 is a DC-37P type connector with jack nuts.
- 4. Units supplied with mating connectors and power cord.
- 5. Weight: 2.63 lbs. max.

WARRANTY

All units are warranted against defects in materials and workmanship for 1 year from the date of shipment. Liability is expressly limited to servicing, adjusting or replacing any CSI product returned to our factory with delivery charges prepaid. In no case shall our liability exceed the original purchase price.